Error Definition										
Comments										
Time Stamp	2003/09/15 08:50	2003/09/15 08:50	2003/09/15 08:55	2003/09/16 11:16	2003/09/15 14:52	2003/09/16 11:19	2003/09/16 11:17	2003/09/16 11:20	2003/09/16 11:20	2003/09/16 11:20
DBs	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/09/15 08:50 DERWENT	USPAT; US-PGPUB; 2003/09/15 08:55	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/09/16 11:19	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/09/16 11:20 DERWENT
Search Text	fpga same programmable same linterconnect	fpga same programmable same interconnect and rows and columns and lorizontal and vertical	fpga same programmable same interconnect and rows and columns and horizontal and vertical and interface	fpga same programmable same interconnect and rows and columns and USPAT; US-PGPUB; 2003/09/16 11:16 horizontal and vertical and interface and diagonal	freeway same gate same array	freeway same array and architecture	fpga same programmable same interconnect and rows and columns and USPAT; US-PGPUB; 2003/09/16 11:17 horizontal and vertical and interface and diagonal and bitstream	freeway same array and architecture and USPAT; US-PGPUB; 2003/09/16 11:20 bitstream	freeway same array and architecture and USPAT; US-PGPUB; 2003/09/16 11:20 netlist	freeway same netlist and routing
Hits	957	245	149	52	0	6	12	0	0	0
Type	BRS	BRS	BRS	BRS	BRS	BRS	BRS	BRS	BRS	BRS
	47	48	49	20	56	22	99	29	89	69